



UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **NAKAMURA, Shunji, et al.**

Serial Number: 10/708,792

Filed: **March 25, 2004**

For. **SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING  
SAME**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

March 25, 2004

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and  
Trademark Office the references listed on the attached PTO-1449.

**Reference AI is discussed at page 5 of the specification.**

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please  
charge Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS,  
HANSON & BROOKS, LLP

Mel R. Quintos  
Attorney for Applicants  
Reg. No. 31,898

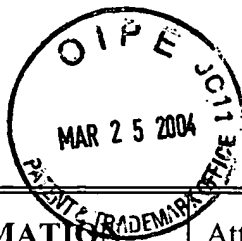
MRQ/lrj  
Atty. Docket No. 040126  
Suite 1000  
1725 K Street, N.W.  
Washington, D.C. 20006  
(202) 659-2930



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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (5)



**INFORMATION  
DISCLOSURE  
STATEMENT  
PTO-1449**

Atty. Docket No. **040126**

Serial No. **10/708,792**

Applicant(s): **NAKAMURA, Shunji, et al.**

Filing Date: **March 25, 2004**

Group Art Unit: **Not Yet Assigned**

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					

**FOREIGN PATENT DOCUMENTS**

Document No.	Date	Country	Translation (Yes or No)
_____	AC		
_____	AD		
_____	AE		

**OTHER DOCUMENTS**

_____	AF	Patent Abstracts of Japan No. 08-186249 (Abstract)
_____	AG	Patent Abstracts of Japan No. 05-121450 (Abstract)
_____	AH	Patent Abstracts of Japan No. 2000-012791 (Abstract)
_____	AI	Patent Abstracts of Japan No. 2000-022158 (Abstract)
_____	AJ	Patent Abstracts of Japan No. 2000-286418 (Abstract)
Examiner _____ Date Considered _____		